

Analog Circuit Design: Low Voltage Low Power; Short Range Wireless Front-Ends; Power Management And DC-DC

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Analog Circuit Design: Low Voltage Low Power; Short Range Wireless Front-Ends; Power Management and DC-DC by Michiel Steyaert, Arthur van Roermund, Andrea Baschirotto

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Analog and power management|freescale

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Edward Lee University of Texas Date: April 12, 2004 Time: 11:00 am Location: Bourns Hall A265 The supply voltage for advanced CMOS processes have been reduced

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